

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)
Think Excellence. Live Excellence.
Shri VaishnavSM Institute of Technology and Science, Indore
Department of Electricals and Electronics Engineering



Time Table - Jul-Dec 2021

Doc No. : SVVV/AC/002		Rev No. :				Rev. Date :	
Class : III Yr. EC-IOT		Block: Department of Electrical and Electronics Engineering				Room No. : Online Mode	
Effective From: 9 Aug 2021		Total Load / Week: 24				Class Teacher: Mr. Nilesh Patidar Mrs. Mausami Verma	
TIME/DAY	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	
L1	BTCS405	BTECIOT502	BTEC501	BTCS405	BTCS405 T	BTEC501	
01:45 - 02:30	AS	SKJ	NP	AS	AS	NP	
L2	BTEC504 T BTEC515 BTEI611	BTIT406	BTECIOT502	BTECIOT502	BTECIOT501 P	BTEC504 BTEC515 BTEI611	
02:30 - 03:15	SH MV KT	PJ	SKJ	SKJ	CS	SH MV KT	
L3	BTEC501	BTECIOT501	BTEC504 BTEC515 BTEI611	BTEC504 P BTEC515 BTEI611	BTEC504 BTEC515 BTEI611	BTECIOT501	
03:15 - 04:00	NP	CS	SH MV KT	SH MV KT	SH MV KT	CS	
L4	BTECIOT501	BTCS405	BTECIOT503	BTCS405 P	BTEC501 T	BTEC501 P	
04:00 - 04:45	CS	AS	SG	AS	NP	NP	

L-Lecture, T-Tutorial, P-Practical, E1- Elective1

S No.	Sub. Code	Subject Name	L	T	P	C	Name of Faculty	Abb
1	BTEC501	Microprocessor, Microcontroller and Interfacing	3	1	2	5	Mr. Nilesh Patidar	NP
2	BTCS405	Data Base Management System	3	1	2	5	Mr. Ashish Sharma	AS
3	BTECIOT501	Communication Systems	3	0	2	4	Mrs. Chanchal Soni	CS
4	BTECIOT502	Wireless and Mobile Communication	3	0	0	3	Mr. Sandeep Kumar Jain	SKJ
5	BTEC504 BTEC515 BTEI611	CMOS VLSI Design(2) Data Communication and Computer N/W(16) Data Acquisition system(5)	3	1	2	5	Mr. Shiraz Husain Mrs. Mausmi Verma Mrs. Kusum Tilkar	SH MV KT
6	BTECIOT503	Minor Project	0	0	2	1	Mr. Shyam Gehlot	SG
7	BTIT406	Unix and Shell Programming Lab	0	0	2	1	Mr. Preet Jain	PJ
			15	3	12	24		

Mr. Shyam Gehlot
Mr. Vikas Jain
Time Table Coordinator
Dept. of EEE

Dr. Namit Gupta
HOD, Dept. of EEE
Director, SVITS

Dr. Shishir Jain
Time Table Coordinator
SVVV